SIEMENS



Data sheet 6ES7314-6CH04-0AB0



SIMATIC S7-300, CPU 314C-2 DP Compact CPU with MPI, 24 DI/16 DO, 4 AI, 2 AO, 1 Pt100, 4 high-speed counters (60 kHz), integrated DP interface, Integr. power supply 24 V DC, work memory 192 KB, Front connector (2x 40-pole) and Micro Memory Card required

Mobile: 00989122160416

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00982136610801

General information	
HW functional status	01
Firmware version	V3.3
Engineering with	
Programming package	STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203
Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines	Miniature circuit breaker, type C; min. 2 A; miniature circuit
(recommendation)	breaker type B, min. 4 A
Mains buffering	
 Mains/voltage failure stored energy time 	5 ms
● Repeat <mark>rate, min.</mark>	1 s
Load voltage L+	
Digital inputs	
— Rated value (DC)	24 V

Doverse pelevity protection	Yes
Reverse polarity protection	165
Digital outputs	24.1/
— Rated value (DC)	24 V
 Reverse polarity protection 	No
Input current	
Current consumption (rated value)	880 mA
Current consumption (in no-load operation), typ.	150 mA
Inrush current, typ.	5 A
l²t	0.7 A ² ·s
Digital inputs	
• from load voltage L+ (without load), max.	80 mA
Digital outputs	
● from load voltage L+, max.	50 mA
Power loss	
Power loss, typ.	13 W
Memory	
Work memory	
• integrated	192 kbyte
• expandable	No
Size of retentive memory for retentive data	64 kbyte
blocks	
Load memory	Vac
• Plug-in (MMC)	Yes
● Plug-in (MMC), max.	8 Mbyte
 Data management on MMC (after last programming), min. 	10 y
Backup	
• present	Yes; Guaranteed by MMC (maintenance-free)
without battery	Yes; Program and data
CPU processing times	
for bit operations, typ.	0.06 µs
for word operations, typ.	0.12 µs
for fixed point arithmetic, typ.	0.16 µs
for floating point arithmetic, typ.	0.59 µs
	· ·
CPU-blocks	1004 (DD 50 5D) (I
Number of blocks (total)	1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
DB	can be reduced by the IVIIVIO used.
	1 024; Number range: 1 to 16000
• Number, max.	
• Size, max.	64 kbyte
FB	

- N I	4 024 Number renge: 0 to 7000
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
FC	
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
OB	
Description	see instruction list
• Size, max.	64 kbyte
 Number of free cycle OBs 	1; OB 1
 Number of time alarm OBs 	1; OB 10
 Number of delay alarm OBs 	2; OB 20, 21
 Number of cyclic interrupt OBs 	4; OB 32, 33, 34, 35
 Number of process alarm OBs 	1; OB 40
 Number of DPV1 alarm OBs 	3; OB 55, 56, 57
 Number of startup OBs 	1; OB 100
 Number of asynchronous error OBs 	5; OB 80, 82, 85, 86, <mark>87</mark>
 Number of synchronous error OBs 	2; OB 121, 122
Nesting depth	
• per priority class	16
 additional within an error OB 	4

Counters, timers and their retentivity	X
S7 counter	
Number	256
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	255
— preset	Z 0 to Z 7
Counting range	
— lower limit	0
— upper limit	999
IEC counter	
• present	Yes
● Type	SFB
Number	Unlimited (limited only by RAM capacity)
S7 times	
Number	256
Retentivity	
— adjustable	Yes
— lo <mark>wer limit</mark>	0
— upper limit	255

Time range - lower limit 10 ms 9990 s IEC timer • present Yes SFB • Type SFB • Number Unlimited (limited only by RAM capacity) Data areas and their retentivity retentive data area in total all, max. 64 KB Flag • Number, max. 256 byte • Number of clock memories 8; 1 memory byte Data blocks • Retentivity preset MB 0 to MB 15 • Number of clock memories 8; 1 memory byte Data blocks • Retentivity preset Yes • Retentivity preset Yes Address area I/O address area • Inputs 2 048 byte • Outputs 2 048 byte • Outputs 2 048 byte • Outputs, adjustable 2 048 byte • Inputs, adjustable 2 048 byte • Outputs, adjustable 2 048 byte • Outputs, adjustable 2 048 byte • Outputs, adjustable 2 048 byte • Inputs 2 048 byte • Outputs, adjustable 2 048 byte • Outputs, adjustable	— preset	No retentivity
upper limit 9 990 s IEC timer • present Yes • Type SFB • Number Unlimited (limited only by RAM capacity) Data areas and their retentivity retentive data area in total all, max. 64 KB Flag • Number, max. 256 byte • Retentivity preset MB to to MB 15 • Number of clock memories 8; 1 memory byte Data blocks • Retentivity adjustable Yes; via non-retain property on DB • Retentivity preset Yes Address area I/O address area I/O address area • Inputs • Outputs 2 048 byte • Outputs Process image • Inputs, adjustable 2 048 byte • Outputs, adjustable 2 048 byte • Outputs, adjustable 2 048 byte • Outputs, default 128 byte • Inputs, default 128 byte Default addresses of the integrated channels — Digital inputs — Digital inputs — Digital inputs — Analog outputs 752 to 761 — Analog outputs 16 048 — Analog outputs 10 088 • Inputs — of which central 1068 Analog channels • Inputs — of which central 1008 Analog channels • Inputs • Inputs • Inputs — of which central • Inputs — of which central • Inputs • Inputs — of which central • Inputs • Inputs — of which central • Inputs • Inputs • Inputs — of which central	Time range	. * *
Forestable For	— lower limit	10 ms
Present Type Number Unlimited (limited only by RAM capacity) Data areas and their retentivity retentive data area in total Flag Number, max. Retentivity preset Number of clock memories Number of clock memories Data blocks Retentivity adjustable Retentivity preset Retentivity preset Yes Address area I/O address area I/O address area I/O address area I/O address area Process image I/O cutputs Outputs Outputs, adjustable Outputs, adjustable Outputs, adjustable Outputs, adfault Outputs, default Outputs Outputs Outputs, default Outputs	— upper limit	9 990 s
Type Number Number Unlimited (limited only by RAM capacity) Data areas and their retentivity retentive data area in total Flag Number, max. Retentivity preset Number of clock memories Number of clock memories Retentivity adjustable Retentivity preset Retentivity adjustable Retentivity preset Retentivity preset Retentivity adjustable Retentivity preset Retentivity	IEC timer	
Number Unlimited (limited only by RAM capacity) Data areas and their retentivity retentive data area in total all, max. 64 KB Flag Number, max. 256 byte Number of clock memories B; 1 memory byte Data blocks Retentivity preset Yes: via non-retain property on DB Retentivity preset Yes Address area Vio address area Vio address area Inputs 2048 byte Outputs 2048 byte Outputs 2048 byte Outputs 2048 byte Outputs 2048 byte Inputs, adjustable 2048 byte Outputs, adjustable 2048 byte Outputs, adjustable 2128 byte Outputs, default 128 byte Outputs, default 128 byte Default addresses of the integrated channels — Digital inputs 124.0 to 126.7 — Analog inputs 752 to 761 — Analog outputs 752 to 755 Digital channels • Inputs 108 Analog channels • Inputs 1006	• present	Yes
Data areas and their retentivity retentive data area in total Flag Number, max. Retentivity preset Number of clock memories Number of clock memories Retentivity adjustable Retentivity preset Retentivity preset Yes Address area I/O address a	• Type	SFB
retentive data area in total Flag Number, max. Retentivity preset Number of clock memories Retentivity preset Address area I/O address area I/O address	• Number	Unlimited (limited only by RAM capacity)
Flag Number, max. Retentivity preset Number of clock memories Number of clock memories Retentivity adjustable Retentivity preset Retentivity adjustable Retentivity preset Retentivity adjustable Retentivity and preserved page via non-retain property on DB Retentivity adjustable Retentivity and non-retain property on DB Retentivity and non-retain pro	Data areas and their retentivity	
Number, max. Retentivity preset Retentivity preset Number of clock memories Retentivity adjustable Retentivity adjustable Retentivity preset Retentivity property on DB Retentivity property on DB Retentivity property on DB Retentivity property on DB Retentivity preset Retentivit	retentive data area in total	all, max. 64 KB
Retentivity preset Number of clock memories Retentivity adjustable Retentivity preset Re	Flag	
Number of clock memories Pata blocks Retentivity adjustable Retentivity preset Yes Address area I/O address area I/O address area I/O uputs Outputs Inputs Outputs Outputs Inputs Outputs Outputs Inputs Outputs Inputs Outputs Inputs Outputs Outputs Outputs Outputs Outputs Outputs Outputs Outputs Outputs, adjustable Outputs, adjustable Outputs, default Outputs Ou	• Number, max.	256 byte
Data blocks		
Retentivity adjustable Retentivity preset Retentivity preset Retentivity preset Retentivity preset Retentivity preset Retentivity adjustable Inputs Retentivity adjustable Retentivity preset Retentivity adjustable Retentivity preset Retentivity adjustable Retentivity preset Retentivity adjustable Retentivity preset R	Number of clock memories	8; 1 memory byte
Retentivity preset Address area I/O address area Inputs		
Address area I/O address area I Inputs Outputs Outputs Process image Inputs Outputs Outputs Outputs Outputs Outputs Outputs Outputs, adjustable Outputs, adjustable Outputs, default Outputs O		
	Retentivity preset	Yes
	Address area	
Outputs Process image Inputs Outputs Outputs Outputs Outputs, adjustable Outputs, adjustable Outputs, default Outputs, default Outputs, default Outputs, default Outputs, default Outputs, default Outputs outputs Outputs outputs Outputs Default addresses of the integrated channels Outputs	I/O address area	
Process image	• Inputs	
 Inputs Outputs Outputs Inputs, adjustable Outputs, adjustable Outputs, adjustable Outputs, default Inputs, default Outputs, default Outputs, default Default addresses of the integrated channels — Digital inputs — Digital outputs — Analog inputs — Analog outputs Inputs Outputs Outputs Outputs Outputs Outputs Inputs One Inputs Inputs Inputs Inputs 	Outputs	2 048 byte
 Outputs Inputs, adjustable Outputs, adjustable Outputs, default Inputs, default Outputs, default Outputs, default Default addresses of the integrated channels — Digital inputs — Digital outputs — Digital outputs — Analog inputs — Analog outputs T52 to 761 — Analog outputs Inputs — of which central Outputs — of which central Outputs — of which central Inputs 	Process image	
 Inputs, adjustable Outputs, adjustable Inputs, default Outputs, default Outputs, default Default addresses of the integrated channels — Digital inputs — Digital outputs — Analog inputs — Analog outputs — Analog outputs Inputs Inputs Inputs Outputs — of which central Outputs — of which central Inputs 	• Inputs	, <u>'</u>
 Outputs, adjustable Inputs, default Outputs, default Outputs, default Default addresses of the integrated channels — Digital inputs — Digital outputs — Digital outputs — Analog inputs — Analog outputs T52 to 761 — Analog outputs Digital channels Inputs Outputs — of which central Outputs — of which central 1 008 Analog channels Inputs 1 008 Analog channels Inputs 1 006 	Outputs	2 048 byte
 Inputs, default Outputs, default Default addresses of the integrated channels — Digital inputs — Digital outputs — Digital outputs — Analog inputs — Analog outputs T52 to 761 — Analog outputs T52 to 755 Digital channels Inputs Outputs Outputs Outputs — of which central 1 008 Analog channels Inputs Inputs Inputs Inputs 16 096 — of which central I 008 Analog channels Inputs Inputs 1 006	Inputs, adjustable	
 Outputs, default Default addresses of the integrated channels — Digital inputs — Digital outputs — Digital outputs — Analog inputs — Analog outputs T52 to 761 — Analog outputs T52 to 755 Digital channels Inputs Inputs Outputs Outputs Outputs In 096 Analog channels Inputs Inputs Inputs Inputs Inputs Analog channels Inputs Inputs Inputs 	Outputs, adjustable	
Default addresses of the integrated channels - Digital inputs - Digital outputs - Digital outputs - Analog inputs - Analog outputs - Analog outputs It is in the provided and	Inputs, default	
 — Digital inputs — Digital outputs — Analog inputs — Analog outputs → Analog outputs → Inputs → Outputs — of which central — of which ce	Outputs, default	128 byte
— Digital outputs — Analog inputs — Analog outputs	Default addresses of the integrated channels	
— Analog inputs — Analog outputs 752 to 761 752 to 755 Digital channels Inputs Inputs I 048 — of which central I 016 Outputs I 6 096 — of which central I 1 008 Analog channels I I nputs I 1 006	— Digital inputs	124.0 to 126.7
— Analog outputs 752 to 755 Digital channels 16 048 ● Inputs 1 016 ● Outputs 16 096 — of which central 1 008 Analog channels 1 006	— Digital outputs	124.0 to 125.7
Digital channels • Inputs 16 048 — of which central 1 016 • Outputs 16 096 — of which central 1 008 Analog channels 1 006	— Analog inputs	752 to 761
 Inputs — of which central — 1008 Analog channels — Inputs — 1006 	— Analog outputs	752 to 755
— of which central 1 016 ■ Outputs 16 096 — of which central 1 008 Analog channels ■ Inputs 1 006	Digital channels	
● Outputs 16 096 — of which central 1 008 Analog channels 1 006	• Inputs	
— of which central 1 008 Analog channels ● Inputs 1 006	— of which central	
Analog channels ● Inputs 1 006	Outputs	
• Inputs 1 006		1 008
	Analog channels	
— of which central 253		
	— of which central	253

Outputs	1 007
— of which central	250
Hardware configuration	
Number of expansion units, max.	3
Number of DP masters	
• integrated	1
• via CP	4
Number of operable FMs and CPs (recommended)	
• FM	8
● CP, PtP	8
• CP, LAN	10
Rack	
● Racks, max.	4
Modules per rack, max.	8; In rack 3 max. 7
Time of day	
Time of day	
Clock	
	Yes
Clock	Yes Yes
Clock • Hardware clock (real-time)	
Clock • Hardware clock (real-time) • retentive and synchronizable	Yes
Clock • Hardware clock (real-time) • retentive and synchronizable • Backup time	Yes 6 wk; At 40 °C ambient temperature
Clock • Hardware clock (real-time) • retentive and synchronizable • Backup time • Deviation per day, max.	Yes 6 wk; At 40 °C ambient temperature 10 s; Typ.: 2 s
Clock Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup	Yes 6 wk; At 40 °C ambient temperature 10 s; Typ.: 2 s Clock continues running after POWER OFF Clock continues to run with the time at which the power failure
Clock • Hardware clock (real-time) • retentive and synchronizable • Backup time • Deviation per day, max. • Behavior of the clock following POWER-ON • Behavior of the clock following expiry of backup period	Yes 6 wk; At 40 °C ambient temperature 10 s; Typ.: 2 s Clock continues running after POWER OFF Clock continues to run with the time at which the power failure
Clock • Hardware clock (real-time) • retentive and synchronizable • Backup time • Deviation per day, max. • Behavior of the clock following POWER-ON • Behavior of the clock following expiry of backup period Operating hours counter	Yes 6 wk; At 40 °C ambient temperature 10 s; Typ.: 2 s Clock continues running after POWER OFF Clock continues to run with the time at which the power failure occurred
Clock Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period Operating hours counter Number	Yes 6 wk; At 40 °C ambient temperature 10 s; Typ.: 2 s Clock continues running after POWER OFF Clock continues to run with the time at which the power failure occurred
Clock Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period Operating hours counter Number Number	Yes 6 wk; At 40 °C ambient temperature 10 s; Typ.: 2 s Clock continues running after POWER OFF Clock continues to run with the time at which the power failure occurred
Clock Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period Operating hours counter Number Number Range of values	Yes 6 wk; At 40 °C ambient temperature 10 s; Typ.: 2 s Clock continues running after POWER OFF Clock continues to run with the time at which the power failure occurred 1 0 0 to 2^31 hours (when using SFC 101)
Clock Hardware clock (real-time) retentive and synchronizable Backup time Deviation per day, max. Behavior of the clock following POWER-ON Behavior of the clock following expiry of backup period Operating hours counter Number Number/Number range Range of values Granularity	Yes 6 wk; At 40 °C ambient temperature 10 s; Typ.: 2 s Clock continues running after POWER OFF Clock continues to run with the time at which the power failure occurred 1 0 0 to 2^31 hours (when using SFC 101) 1 h

Digital inputs	
Number of digital inputs	24
 of which inputs usable for technological functions 	16
integrated channels (DI)	24

Yes

Yes

Yes

No

• to MPI, master

• to MPI, slave

• in AS, master

• in AS, slave

Input characteristic curve in accordance with IEC 61131, type 1	Yes
Number of simultaneously controllable inputs	
horizontal installation	
— up to 40 °C, max.	24
— up to 60 °C, max.	12
vertical installation	
— up to 40 °C, max.	12
Input voltage	
• Rated value (DC)	24 V
• for signal "0"	-3 to +5V
• for signal "1"	+15 to +30 V
Input current	
● for signal "1", typ.	8 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.)
— Rated value	3 ms
for technological functions	
— at "0" to "1", max.	8 µs; Minimum pulse width/minimum pause between pulses at maximum counting frequency
Cable length	
• shielded, max.	1 000 m; 50 m for technological functions
• unshielded, max.	600 m; for technological functions: No
for technological functions	
— shielded, max.	50 m; at maximum count frequency
— unshielded, max.	not allowed
Digital outputs	
Number of digital outputs	16
of which high-speed outputs	4; Notice: You cannot connect the fast outputs of your CPU in parallel
integrated channels (DO)	16
Short-circuit protection	Yes; Clocked electronically
 Response threshold, typ. 	1 A
Limitation of inductive shutdown voltage to	L+ (-48 V)
Controlling a digit <mark>al input</mark>	Yes
Switching capacity of the outputs	
● on <mark>lamp load, ma</mark> x.	5 W
Load resistance range	
● lower limit	48 Ω

• upper limit	4 kΩ
Output voltage	
● for signal "1", min.	L+ (-0.8 V)
Output current	
• for signal "1" rated value	500 mA
• for signal "1" permissible range, min.	5 mA
• for signal "1" permissible range, max.	0.6 A
• for signal "1" minimum load current	5 mA
• for signal "0" residual current, max.	0.5 mA
Parallel switching of two outputs	
• for uprating	No
• for redundant control of a load	Yes
Switching frequency	
with resistive load, max.	100 Hz
• with inductive load, max.	0.5 Hz
• on lamp load, max.	100 Hz
• of the pulse outputs, with resistive load, max.	2.5 kHz
Total current of the outputs (per group)	
horizontal installation	
— up to 40 °C, max.	3 A
— up to 60 °C, max.	2 A 🕠
vertical installation	
— up to 40 °C, max.	2 A
Cable length	
• shielded, max.	1 000 m
• unshielded, max.	600 m
Analog inputs	
Number of analog inputs	5
For voltage/current measurement	4
For resistance/resistance thermometer	1
measurement	
integrated channels (AI)	5; 4x current/voltage, 1x resistance
permissible input voltage for current input (destruction limit), max.	5 V; Permanent
permissible input voltage for voltage input (destruction limit), max.	30 V; Permanent
permissible input current for voltage input (destruction limit), max.	0.5 mA; Permanent
permissible input current for current input (destruction limit), max.	50 mA; Permanent
Technical unit for temperature measurement adjustable	Yes; Degrees Celsius / degrees Fahrenheit / Kelvin
Input ranges	
7 4	

Voltage	Yes; ±10 V / 100 kΩ; 0 V to 10 V / 100 kΩ
Current	Yes; ± 20 mA / 100 Ω ; 0 mA to 20 mA / 100 Ω ; 4 mA to 20 mA / 100 Ω
• Desigtance the management of	Yes; Pt 100 / 10 MΩ
Resistance thermometer	Yes: 0 Ω to 600 Ω / 10 MΩ
Resistance	Yes, 0 12 to 600 12 / 10 Mt2
Input ranges (rated values), voltages	V
• 0 to +10 V	Yes
— Input resistance (0 to 10 V)	100 kΩ
Input ranges (rated values), currents	
• 0 to 20 mA	Yes
— Input resistance (0 to 20 mA)	100 Ω
• -20 mA to +20 mA	Yes
— Input resistance (-20 mA to +20 mA)	100 Ω
• 4 mA to 20 mA	Yes
— Input resistance (4 mA to 20 mA)	100 Ω
Input ranges (rated values), resistance thermometer	
● Pt 100	Yes
— Input resistance (Pt 100)	10 ΜΩ
Input ranges (rated values), resistors	
• 0 to 600 ohms	Yes
— Input resistance (0 to 600 ohms)	10 MΩ
Thermocouple (TC)	
Temperature compensation	
— parameterizable	No
Characteristic linearization	
parameterizable	Yes; by software
Cable length	
• shielded, max.	100 m
Analog outputs	
Number of analog outputs	2
integrated channels (AO)	2
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit current, max.	55 mA
Current output, no-load voltage, max.	14 V
Output ranges, voltage	
• 0 to 10 V	Yes
• -10 V to +10 V	Yes
Output ranges, current	
• 0 to 20 mA	Yes
• -20 mA to +20 mA	Yes
• 4 mA to 20 mA	Yes
Connection of actuators	

 for voltage output two-wire connection 	Yes; Without compensation of the line resistances
 for voltage output four-wire connection 	No
 for current output two-wire connection 	Yes
Load impedance (in rated range of output)	
with voltage outputs, min.	1 kΩ
 with voltage outputs, capacitive load, max. 	0.1 μF
with current outputs, max.	300 Ω
 with current outputs, inductive load, max. 	0.1 mH
Destruction limits against externally applied voltages a	and currents
 Voltages at the outputs towards MANA 	16 V; Permanent
Current, max.	50 mA; Permanent
Cable length	
• shielded, max.	200 m

Analog value generation for the inputs	
Measurement principle	Actual value encryption (successive approximation)
Integration and conversion time/resolution per channel	
 Resolution with overrange (bit including sign), max. 	12 bit
 Integration time, parameterizable 	Yes; 16.6 / 20 ms
 Interference voltage suppression for interference frequency f1 in Hz 	50 / 60 Hz
 permissible input frequency, max. 	400 Hz
 Time constant of the input filter 	0.38 ms
Basic execution time of the module (all channels released)	1 ms

Analog value generation for the outputs		
Integration and conversion time/resolution per channel		
 Resolution with overrange (bit including sign), 	12 bit	
max.		
Conversion time (per channel)	1 ms	
Settling time		
for resistive load	0.6 ms	
● for capacitive load	1 ms	
for inductive load	0.5 ms	

Encoder		
Connection of signal encoders		
• for voltage measurement	Yes	
• for current measurement as 2-wire transducer	Yes; with external supply	
 for current measurement as 4-wire transducer 	Yes	
 for resistance measurement with two-wire connection 	Yes; Without compensation of the line resistances	

 for resistance measurement with three-wire connection for resistance measurement with four-wire connection 	No No
Connectable encoders	
• 2-wire sensor	Yes
 permissible quiescent current (2-wire sensor), max. 	1.5 mA
Errors/accuracies	
Temperature error (relative to input range), (+/-)	0.006 %/K
Crosstalk between the inputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to input range), (+/-)	0.06 %
Output ripple (relative to output range, bandwidth 0 to	0.1 %
50 kHz), (+/-)	

60 dB

0.06 %

1 %

0.01 %/K

Operational error limit in overall temperature range

Repeat accuracy in steady state at 25 °C (relative to

Temperature error (relative to output range), (+/-)

Crosstalk between the outputs, min.

output range), (+/-)

· ·	
 Voltage, relative to input range, (+/-) 	1 %
• Current, relative to input range, (+/-)	1 %
• Resistance, relative to input range, (+/-)	1 %
 Voltage, relative to output range, (+/-) 	1 %

Basic error limit (operational limit at 25 °C)

• Current, relative to output range, (+/-)

• Current, relative to output range, (+/-)

 Voltage, relative to input range, (+/-) 	0.8 %; Linearity error ±0.06 %
• Current, relative to input range, (+/-)	0.8 %; Linearity error ±0.06 %
• Resistance, relative to input range, (+/-)	0.8 %; Linearity error ±0.2 %
• Resistance thermometer, relative to input range, (+/-)	0.8 %
 Voltage, relative to output range, (+/-) 	0.8 %

Interference voltage suppression for f = n x (f1 +/- 1 %), f1 = interference frequency

 Series mode interference (peak value of 	30 dB
interference < rated value of input range), min.	
Common mode interference, min.	40 dB

Interfaces	
Number of industrial Ethernet interfaces	0
Number of PROFINET interfaces	0
Number of RS 485 interfaces	2; MPI and PROFIBUS DP
Number of RS 422 interfaces	0

0.8 %

nterface type	Integrated RS 485 interface
Physics	RS 485
Isolated	No
Power supply to interface (15 to 30 V DC), max.	200 mA
Protocols	
• MPI	Yes
 PROFIBUS DP master 	No
 PROFIBUS DP slave 	No
Point-to-point connection	No
MPI	
Transmission rate, max.	187.5 kbit/s
Services	
— PG/OP communication	Yes
— Routing	Yes
 Global data communication 	Yes
 — S7 basic communication 	Yes
— S7 communication	Yes; Only server, configured on one side
— S7 communication, as client	No; but via CP and loadable FB
— S7 communication, as server	Yes
. Interface	
Interface type	Integrated RS 485 interface

2. Interface		
Interface type	Integrated RS 485 interface	
Physics	RS 485	
Isolated	Yes	
Power supply to interface (15 to 30 V DC), max.	200 mA	
Protocols		
• MPI	No	
PROFINET IO Controller	No	
PROFINET IO Device	No	
PROFINET CBA	No	
PROFIBUS DP master	Yes	
PROFIBUS DP slave	Yes	
Point-to-point connection	No	
PROFIBUS DP master		
• Transmission rate <mark>,</mark> max.	12 Mbit/s	
 Number of DP slaves, max. 	124	
Services		
— PG/OP communication	Yes	
— Routing	Yes	
Global data communication	No	
— S7 basic communication	Yes; I blocks only	

— S7 communication	Yes; Only server, configured on one side
 S7 communication, as client 	No
 S7 communication, as server 	Yes
— Equidistance	Yes
— Isochronous mode	No
— SYNC/FREEZE	Yes
 Activation/deactivation of DP slaves 	Yes
 Number of DP slaves that can be simultaneously activated/deactivated, max. 	8
 Direct data exchange (slave-to-slave communication) 	Yes; as subscriber
— DPV1	Yes
Address area	
— Inputs, max.	2 kbyte
— Outputs, max.	2 kbyte
User data per DP slave	
— Inputs, max.	244 byte
— Outputs, max.	244 byte
PROFIBUS DP slave	
• GSD file	The latest GSD file is available on the Internet (http://www.siemens.com/profibus-gsd)
Transmission rate, max.	12 Mbit/s
automatic baud rate search	Yes; only with passive interface
 Address area, max. 	32
 User data per address area, max. 	32 byte
Services	
— PG/OP communication	Yes
— Routing	Yes; Only with active interface
Global data communication	No
— S7 basic communication	No
— S7 communication	Yes; Only server, configured on one side
— S7 communication, as client	No
— S7 communication, as server	Yes
 Direct data exchange (slave-to-slave communication) 	Yes
— DPV1	No
Transfer memory	
— Inputs	244 byte
— Outputs	244 byte
Communication functions	
PG/OP communication	Yes
Da <mark>ta record rou</mark> ting	Yes

Global data communication	
• supported	Yes
Number of GD loops, max.	8
Number of GD packets, max.	8
 Number of GD packets, transmitter, max. 	8
Number of GD packets, receiver, max.	8
Size of GD packets, max.	22 byte
Size of GD packet (of which consistent), max.	22 byte
S7 basic communication	
• supported	Yes
• User data per job, max.	76 byte
• User data per job (of which consistent), max.	76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET as server)
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loada <mark>ble</mark> FB
 User data per job, max. 	180 kbyte; With PUT/GET
 User data per job (of which consistent), max. 	240 byte; as server
S5 compatible communication	
• supported	Yes; via CP and loadable FC
Number of connections	
• overall	12
usable for PG communication	11
 reserved for PG communication 	1
— adjustable for PG communication, min.	1
— adjustable for PG communication, max.	11
usable for OP communication	11
reserved for OP communication	1
 adjustable for OP communication, min. 	1
 adjustable for OP communication, max. 	11
 usable for S7 basic communication 	8
 reserved for S7 basic communication 	0
 adjustable for S7 basic communication, 	0
min.	
 adjustable for S7 basic communication, 	8
max.	
usable for routing	4; max.
S7 message functions	
Number of login stations for message functions, max.	12; Depending on the configured connections for PG/OP and S7
	basic communication
Process diagnostic messages	Yes

simultaneously active Alarm-S blocks, max.	300
Test commissioning functions	* X *
Status block	Yes; Up to 2 simultaneously
Single step	Yes
Number of breakpoints	4
Status/control	
Status/control variable	Yes
Variables	Inputs, outputs, memory bits, DB, times, counters
 Number of variables, max. 	30
— of which status variables, max.	30
— of which control variables, max.	14
Forcing	
• Forcing	Yes
 Forcing, variables 	Inputs, outputs
 Number of variables, max. 	10
Diagnostic buffer	
• present	Yes
 Number of entries, max. 	500
— adjustable	No
— of which powerfail-proof	100; Only the last 100 entries are retained
Number of entries readable in RUN, max.	499
— adjustable	Yes; From 10 to 499
— preset	10
Service data	
• can be read out	Yes
Interrupts/diagnostics/status information	
Diagnostics indication LED	
Status indicator digital input (green)	Yes
Status indicator digital output (green)	Yes
Integrated Functions	
Number of counters	4; See "Technological Functions" manual
Counting frequency (counter) max.	60 kHz
Frequency measurement	Yes
Number of frequency meters	4; up to 60 kHz (see "Technological Functions" manual)
controlled positioning	Yes
integrated function blocks (closed-loop control)	Yes; PID controller (see "Technological Functions" manual)
PID controller	Yes
Number of pulse outputs	4; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual)
Limit frequency (pulse)	2.5 kHz
Potential separation	

Potential separation digital inputs	
Potential separation digital inputs	Yes
• between the channels	No
• between the channels and backplane bus	Yes
Potential separation digital outputs	
Potential separation digital outputs	Yes
• between the channels	Yes
• between the channels, in groups of	8
 between the channels and backplane bus 	Yes
Potential separation analog inputs	
Potential separation analog inputs	Yes; common for analog I/O
• between the channels	No
between the channels and backplane bus	Yes
Potential separation analog outputs	
Potential separation analog outputs	Yes; common for analog I/O
• between the channels	No
• between the channels and backplane bus	Yes
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Isolation Isolation tested with	600 V DC
Isolation tested with	000 V BC
Ambient conditions	
Ambient temperature during operation	
• min.	0 °C
• max.	60 °C
Configuration	
Configuration software	
• STEP 7	Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or
	higher with HSP 203
STEP 7 Lite	No
Programming	
Command set	see instruction list
Nesting levels	8
System functions (SFC)	see instruction list
 System function blocks (SFB) 	see instruction list
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
— SCL	Yes
— CFC	Yes
— GRAPH	Yes
— HiGraph®	
— Потарно	Yes

Know-how protection		
User program protection/password protection	Yes	
Block encryption	Yes; With S7 block Privacy	
Dimensions		
Width	120 mm	
Height	125 mm	
Depth	130 mm	
Weights		
Weight, approx.	680 g	
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